

Armv7 M Architecture Reference Manual

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Armv7 M Architecture Reference Manual

This manual documents the Microcontroller profile of version 7 of the ARM® Architecture, the ARMv7-M architecture profile. For short definitions of all the ARMv7 profiles see About the ARMv7 architecture, and architecture profiles on page A1-20. The manual has the following parts:

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Documentation - Arm Developer

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This manual documents the Microcontroller profile associated with version 7 of the ARM Architecture (ARMv7-M). For short-form definitions of all the ARMv7 profiles see page A1-1. The manual consists of three parts: Part A The application level programming model and memory model information along with the

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ARMv7-M Architecture Application Level Reference Manual

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ARMv7-M architecture; 3-stage pipeline with branch speculation. Instruction sets: Thumb-1 (entire). Thumb-2 (entire). 32-bit hardware integer multiply with 32-bit or 64-bit result, signed or unsigned, add or subtract after the multiply. 32-bit multiply is 1 cycle, but 64-bit multiply and MAC instructions require extra cycles.

ARM Cortex-M - Wikipedia

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Since 1995, the ARM Architecture Reference Manual has been the primary source of documentation on the ARM processor architecture and instruction set, distinguishing interfaces that all ARM processors are required to support (such as instruction semantics) from implementation details that may vary. The architecture has evolved over time, and version seven of the architecture, ARMv7, defines three architecture "profiles":

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